OIPE IAP18
FEB 06 2006
PATENT & TRADEMARK OFFICE

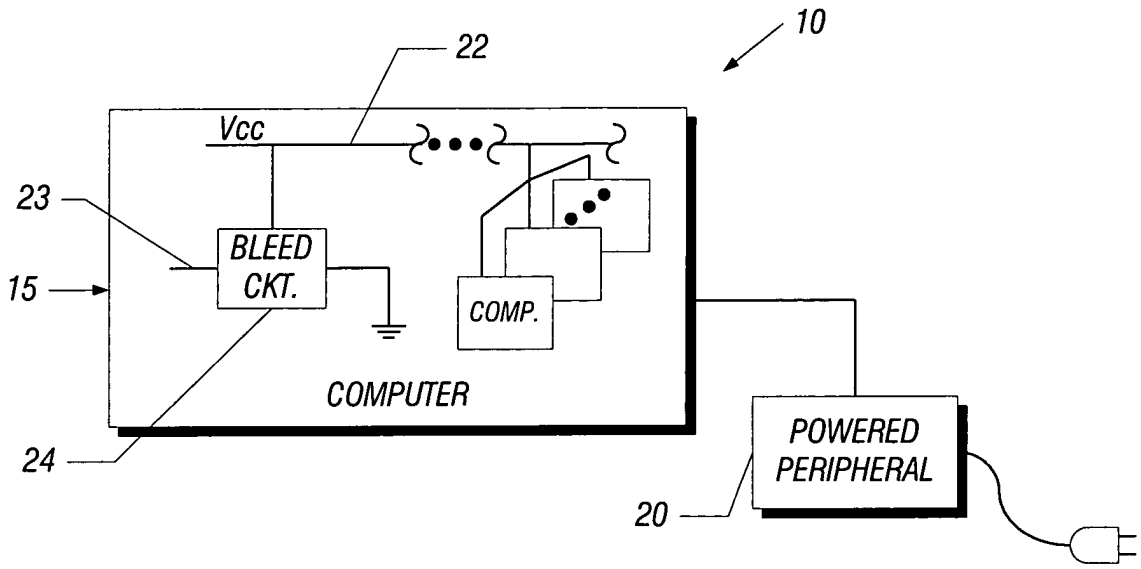


FIG. 1

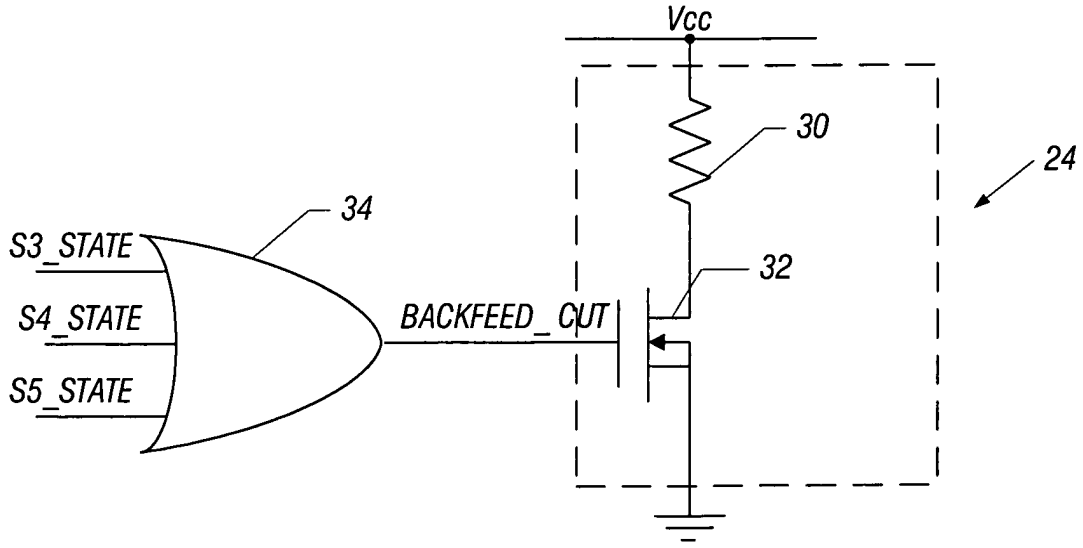


FIG. 2

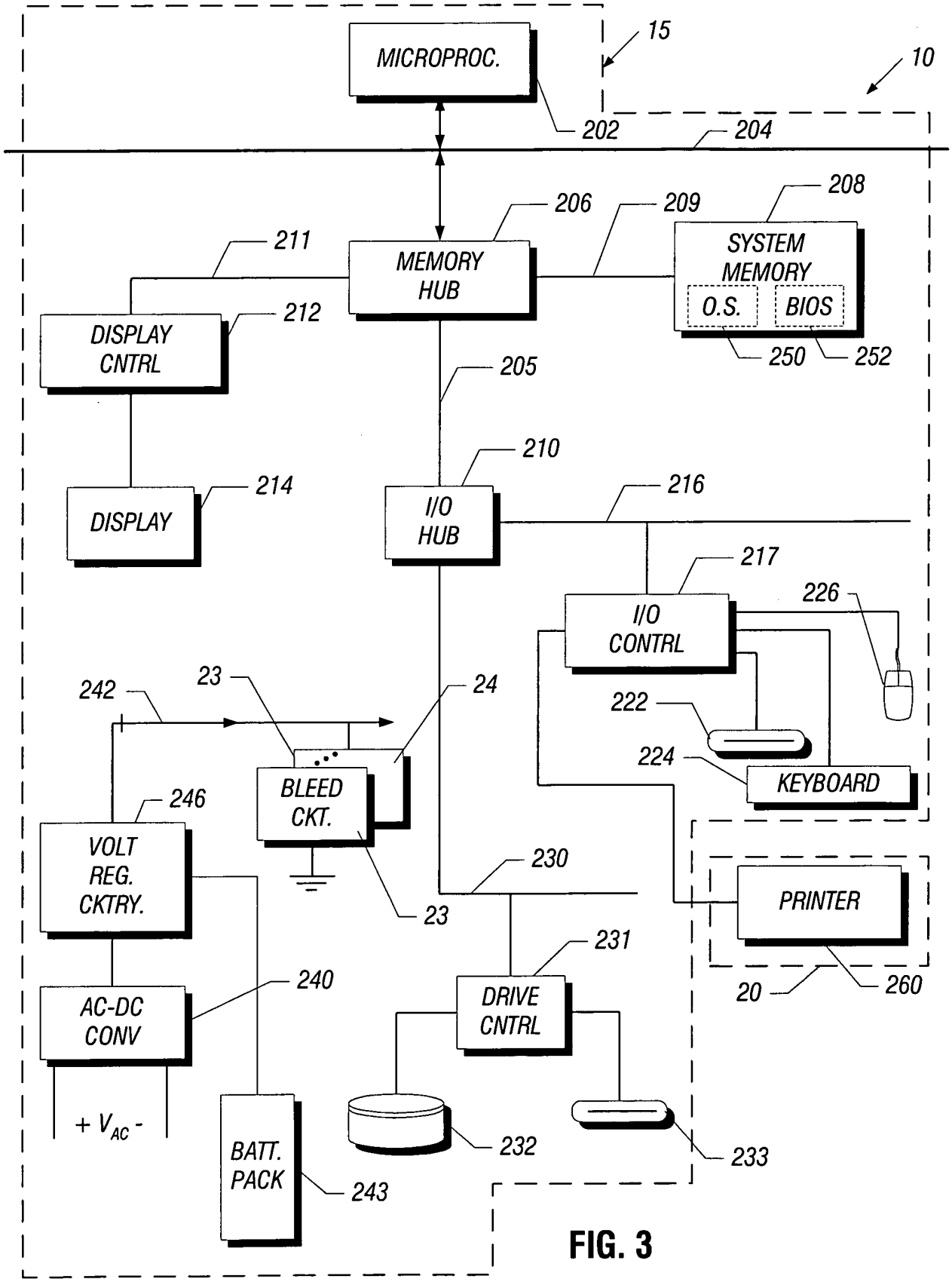
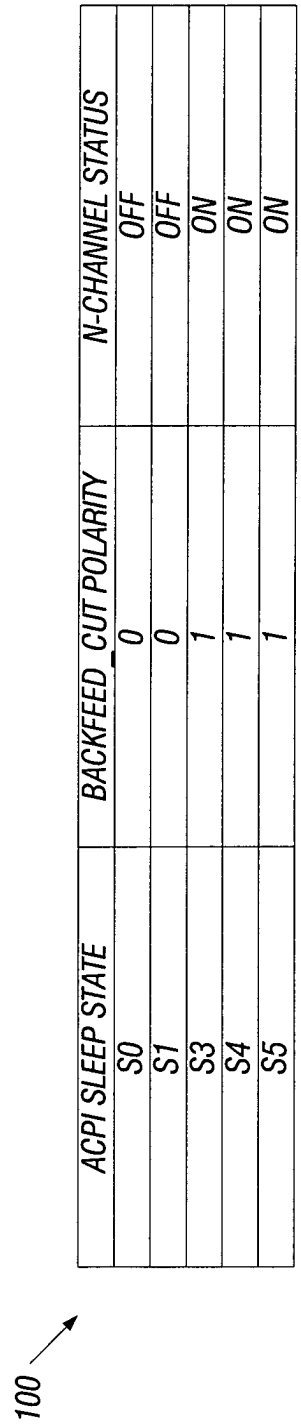

MICROPROC.
15
10
202
204
206
209
208
211
MEMORY HUB
SYSTEM MEMORY
O.S.
BIOS
250
252
DISPLAY CNTRL
212
205
210
214
DISPLAY
I/O HUB
216
217
I/O CONTRL
226
242
23
24
222
224
KEYBOARD
BLEED CKT.
246
VOLT REG. CKTRY.
230
23
PRINTER
231
240
AC-DC CONV
DRIVE CNTRL
20
260
+ V_{AC} -
BATT. PACK
243
232
233

FIG. 3



ACPI SLEEP STATE	BACKFEED_CUT POLARITY	N-CHANNEL STATUS
S0	0	OFF
S1	0	OFF
S3	1	ON
S4	1	ON
S5	1	ON

FIG. 4